

**WEST**

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**Search Results -**

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L3 and ((identificat\$ near2 (information\$2 or data\$2)) same (directory or directories))	1

Database:

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 US Pre-Grant Publication Full-Text Database  
 JPO Abstracts Database  
 EPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

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L5

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**Search History**DATE: Tuesday, October 01, 2002 [Printable Copy](#) [Create Case](#)**Set Name Query**

side by side

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result set

DB=USPT; PLUR=YES; OP=OR

<u>L5</u>	L3 and ((identificat\$ near2 (information\$2 or data\$2)) same (directory or directories))	1	<u>L5</u>
<u>L4</u>	L3 and ((identificat\$ near2 (information\$ or data\$)) same (directory or directories))	1	<u>L4</u>
<u>L3</u>	allocat\$ and EEPROM and (management adj2 area) and application\$	61	<u>L3</u>
<u>L2</u>	allocat\$ same EEPROM same (management adj2 area) same application\$	0	<u>L2</u>
<u>L1</u>	allocation same EEPROM same (management adj2 area) same application	0	<u>L1</u>

END OF SEARCH HISTORY

**WEST**[Generate Collection](#)[Print](#)**Search Results - Record(s) 1 through 1 of 1 returned.**☐ 1. Document ID: US 5590306 A

L5: Entry 1 of 1

File: USPT

Dec 31, 1996

US-PAT-NO: 5590306

DOCUMENT-IDENTIFIER: US 5590306 A

TITLE: Memory card management system for writing data with usage and recording codes made significant

Full	Title	CLS.1	REF.1	SEQ.1	ATT.1

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Terms	Documents
L3 and ((identificat\$ near2 (information\$2 or data\$2)) same (directory or directories))	1

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Terms	Documents
L3 and (identificat\$ near2 (information\$2 or data\$2))	6

US Patents Full-Text Database

US Pre-Grant Publication Full-Text Database

JPO Abstracts Database

EPO Abstracts Database

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Database:

Search:

L6

Refine Search

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**Search History**DATE: Tuesday, October 01, 2002   [Printable Copy](#)   [Create Case](#)**Set Name Query**

side by side

**Hit Count Set Name**

result set

DB=USPT; PLUR=YES; OP=OR

<u>L6</u>	L3 and (identificat\$ near2 (information\$2 or data\$2))	6	<u>L6</u>
<u>L5</u>	L3 and ((identificat\$ near2 (information\$2 or data\$2)) same (directory or directories))	1	<u>L5</u>
<u>L4</u>	L3 and ((identificat\$ near2 (information\$ or data\$)) same (directory or directories))	1	<u>L4</u>
<u>L3</u>	allocat\$ and EEPROM and (management adj2 area) and application\$	61	<u>L3</u>
<u>L2</u>	allocat\$ same EEPROM same (management adj2 area) same application\$	0	<u>L2</u>
<u>L1</u>	allocation same EEPROM same (management adj2 area) same application	0	<u>L1</u>

END OF SEARCH HISTORY

**WEST**[Generate Collection](#)[Print](#)**Search Results - Record(s) 1 through 6 of 6 returned.**☐ 1. Document ID: US 6438086 B1

L6: Entry 1 of 6

File: USPT

Aug 20, 2002

US-PAT-NO: 6438086

DOCUMENT-IDENTIFIER: US 6438086 B1

TITLE: Recording apparatus and method, reproducing apparatus and method, and recording medium

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Draw Desc	Image
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☐ 2. Document ID: US 6388965 B2

L6: Entry 2 of 6

File: USPT

May 14, 2002

US-PAT-NO: 6388965

DOCUMENT-IDENTIFIER: US 6388965 B2

TITLE: Dubbing apparatus and dubbing method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Draw Desc	Image
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☐ 3. Document ID: US 6363456 B1

L6: Entry 3 of 6

File: USPT

Mar 26, 2002

US-PAT-NO: 6363456

DOCUMENT-IDENTIFIER: US 6363456 B1

TITLE: IC card, IC card processing system, and IC card processing method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMNC	Draw Desc	Image
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☐ 4. Document ID: US 6163485 A

L6: Entry 4 of 6

File: USPT

Dec 19, 2000

US-PAT-NO: 6163485

DOCUMENT-IDENTIFIER: US 6163485 A

TITLE: Semiconductor integrated circuit data processing system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMNC	Draw Desc	Image
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☐ 5. Document ID: US 5590306 A

L6: Entry 5 of 6

File: USPT

Dec 31, 1996

US-PAT-NO: 5590306

DOCUMENT-IDENTIFIER: US 5590306 A

TITLE: Memory card management system for writing data with usage and recording codes made significant

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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KMIC	Draw Desc	Image
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☐ 6. Document ID: US 4742215 A

L6: Entry 6 of 6

File: USPT

May 3, 1988

US-PAT-NO: 4742215

DOCUMENT-IDENTIFIER: US 4742215 A

TITLE: IC card system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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KMIC	Draw Desc	Image
------	-----------	-------

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Terms	Documents
L3 and (identificat\$ near2 (information\$2 or data\$2))	6

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**WEST****End of Result Set**☐ **Generate Collection** **Print**

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File: USPT

Jul 6, 1993

US-PAT-NO: 5226145

DOCUMENT-IDENTIFIER: US 5226145 A

TITLE: Storage management system for memory card using memory allocation table

DATE-ISSUED: July 6, 1993

INT-CL: [05] G06F 12/00

US-CL-ISSUED: 395/425; 364/DIG.1, 364/254.4, 364/260, 358/310, 358/335, 358/209

US-CL-CURRENT: 711/202; 348/231.2FIELD-OF-SEARCH: 395/425, 395/400, 364/2MSFile, 364/9MSFile, 371/49.1, 371/51.1,  
371/50.1

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L1: Entry 3 of 4

File: USPT

Dec 31, 1996

US-PAT-NO: 5590306

DOCUMENT-IDENTIFIER: US 5590306 A

TITLE: Memory card management system for writing data with usage and recording codes made significant

DATE-ISSUED: December 31, 1996

INT-CL: [06] G06 F 12/00

US-CL-ISSUED: 395/442; 395/427, 395/430, 395/481, 395/492, 364/DIG.1, 386/46, 348/233

US-CL-CURRENT: 711/115; 348/231.3, 348/231.7, 711/100, 711/103, 711/154, 711/165

FIELD-OF-SEARCH: 395/427, 395/430, 395/442, 395/481, 395/492

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L1: Entry 2 of 4

File: USPT

Jun 3, 1997

US-PAT-NO: 5635703

DOCUMENT-IDENTIFIER: US 5635703 A

TITLE: Card storage medium having a multi-application support function

DATE-ISSUED: June 3, 1997

INT-CL: [06] G06 K 19/06

US-CL-ISSUED: 235/492; 235/487, 235/380, 902/4, 395/730, 395/732, 395/601

US-CL-CURRENT: 235/492; 235/380, 235/487, 707/1, 710/242, 710/244, 902/4FIELD-OF-SEARCH: 235/492, 235/487, 235/380, 235/382, 235/441, 902/4, 902/26,  
395/600, 395/730, 395/732



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L1: Entry 1 of 4

File: USPT

Dec 1, 1998

US-PAT-NO: 5845069

DOCUMENT-IDENTIFIER: US 5845069 A

TITLE: Card-type storage medium protecting data stored in its memory by interrupting an existing transaction after a predetermined permissible number of accesses

DATE-ISSUED: December 1, 1998

INT-CL: [06] G06 F 13/20

US-CL-ISSUED: 395/186; 705/18, 902/1

US-CL-CURRENT: 713/200; 705/18, 902/1

FIELD-OF-SEARCH: 395/186, 395/835, 395/821, 705/18, 705/17, 705/44, 902/1, 235/379, 235/381, 235/380, 380/23, 380/4, 340/825.34, 340/825.31